

FIG. 1

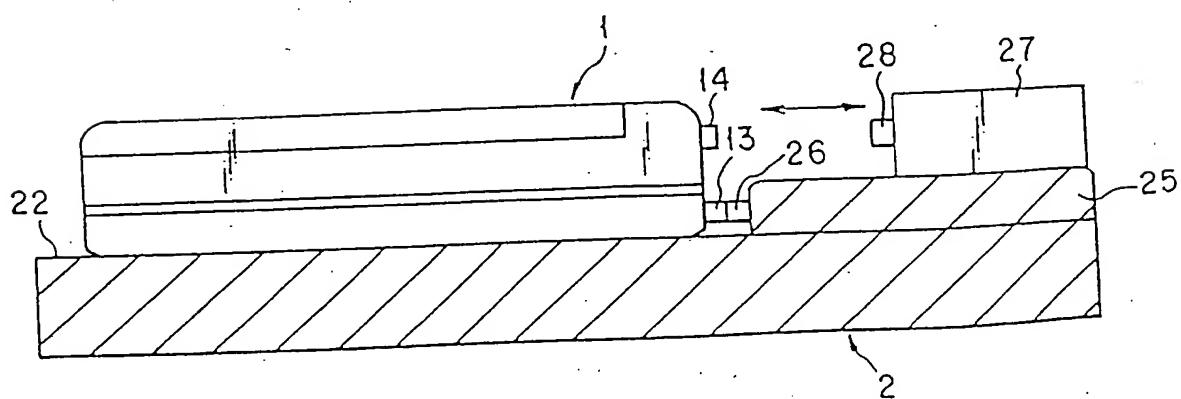


FIG. 2

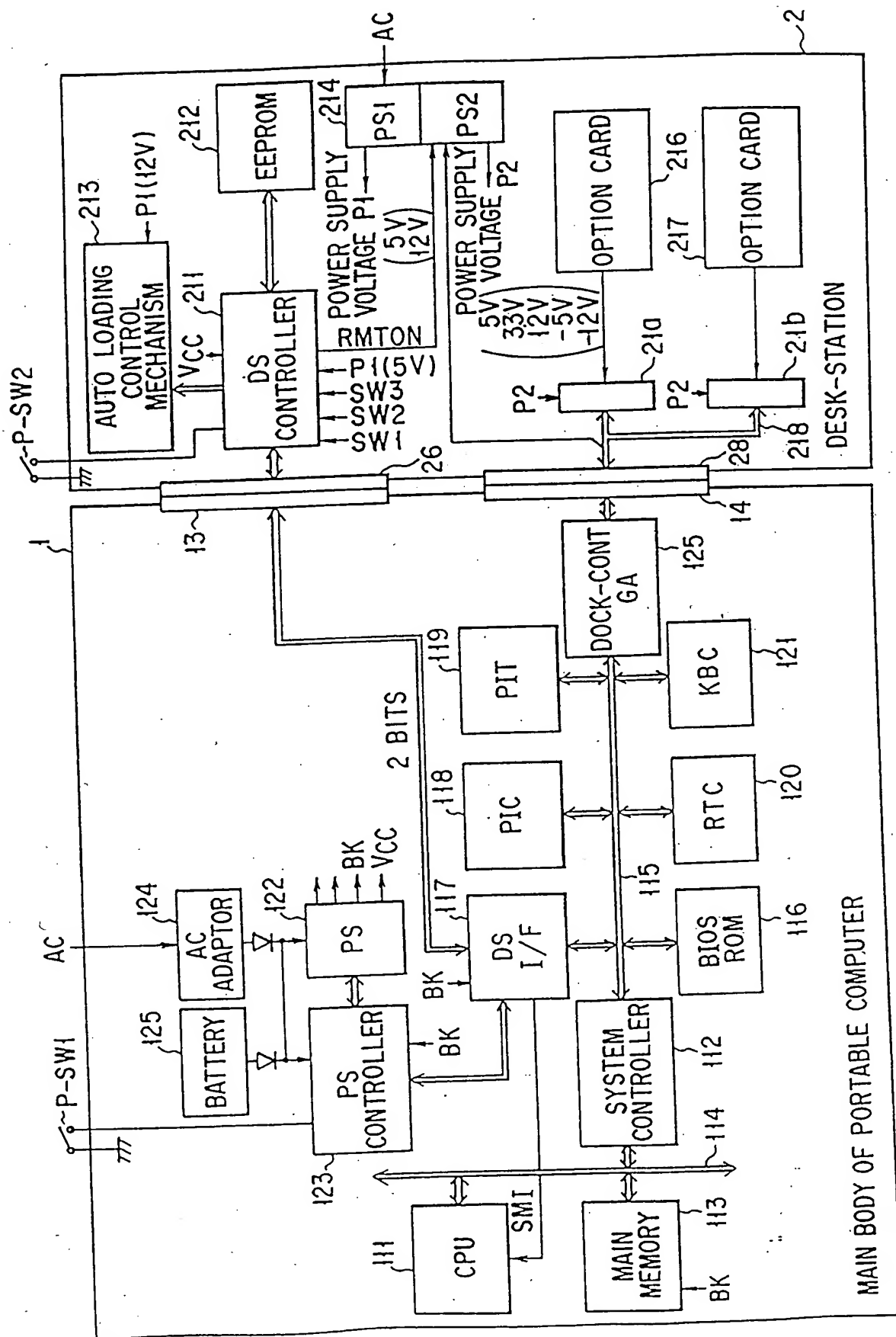


FIG. 3

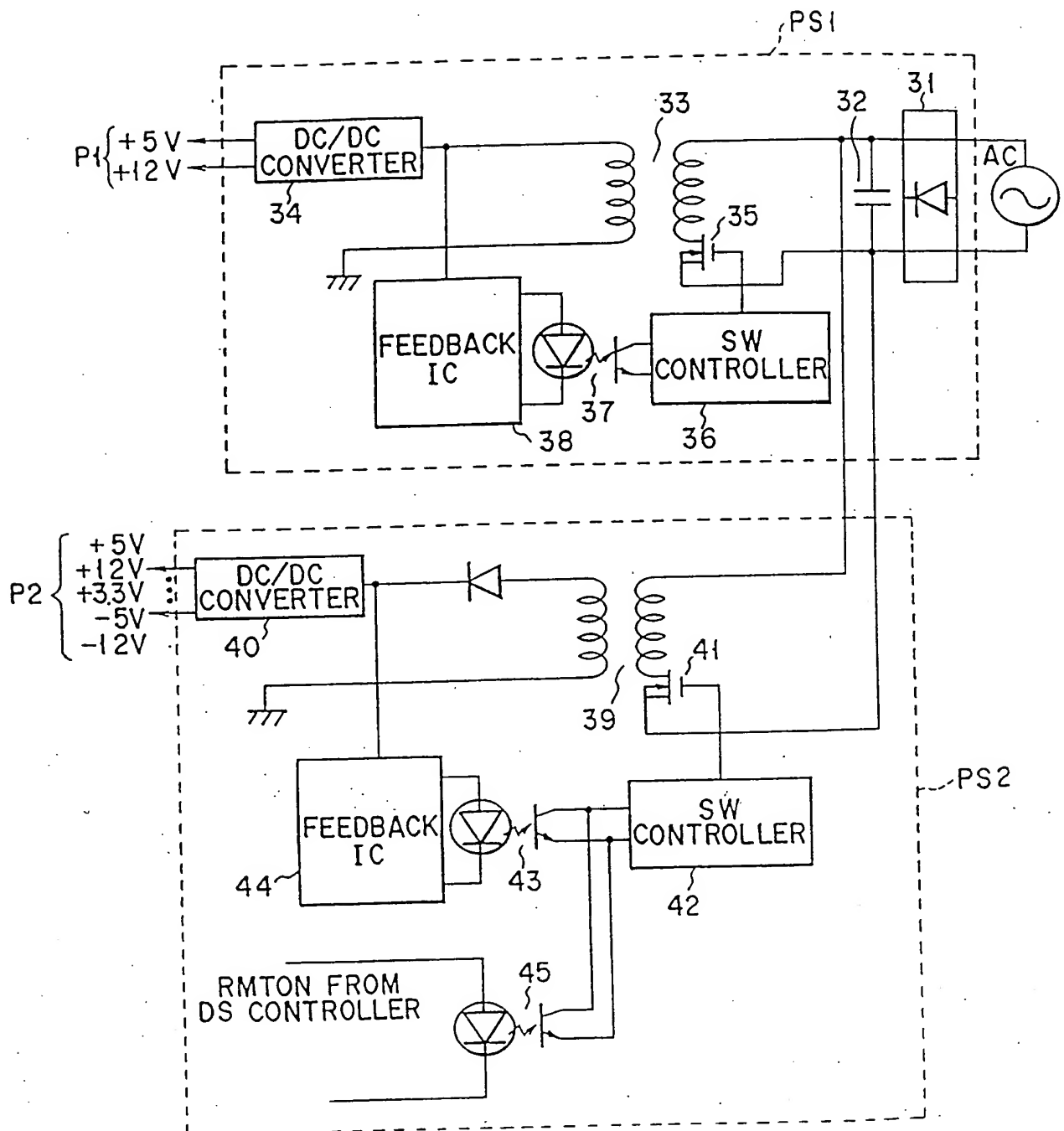


FIG. 4

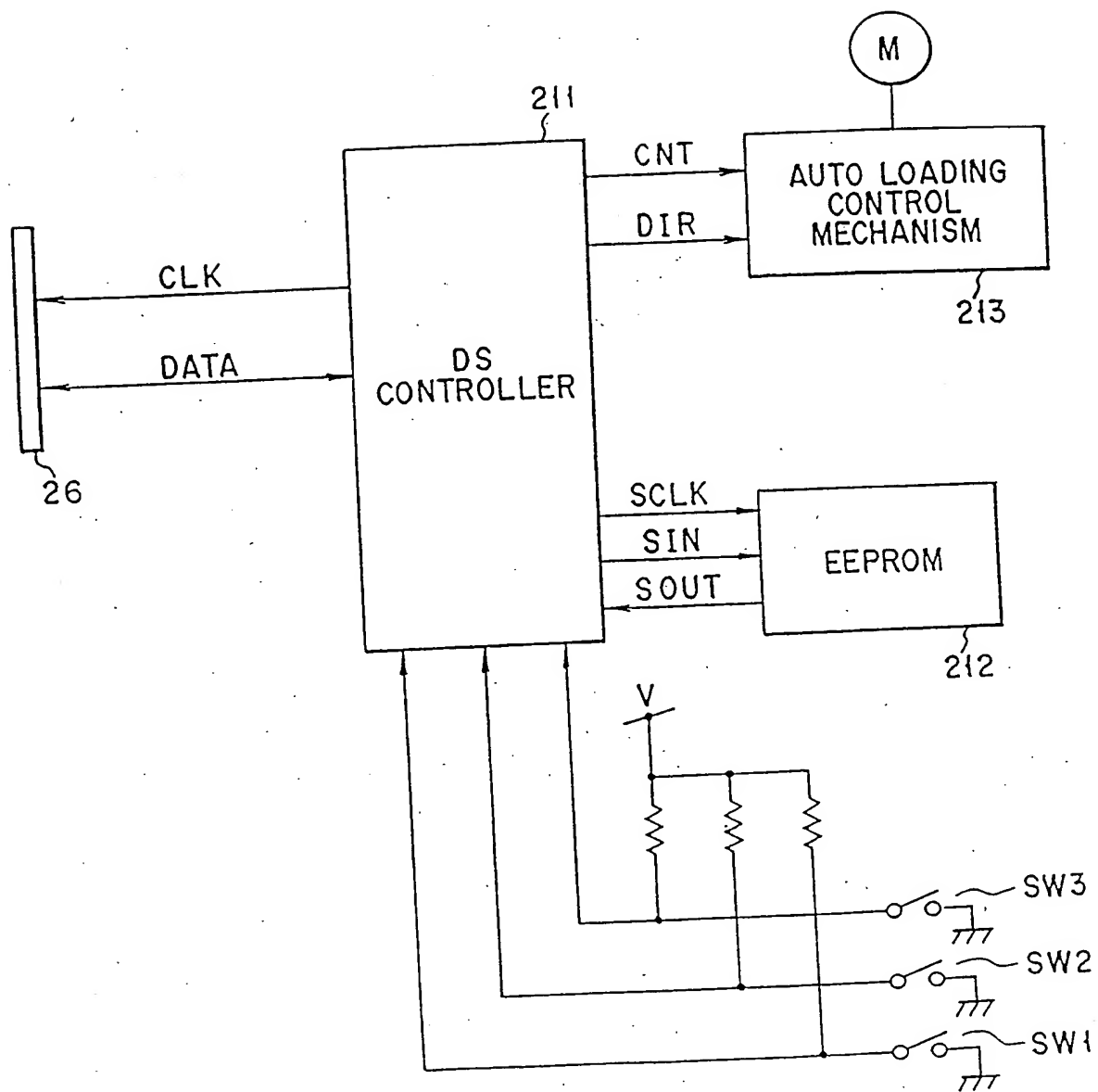


FIG. 5

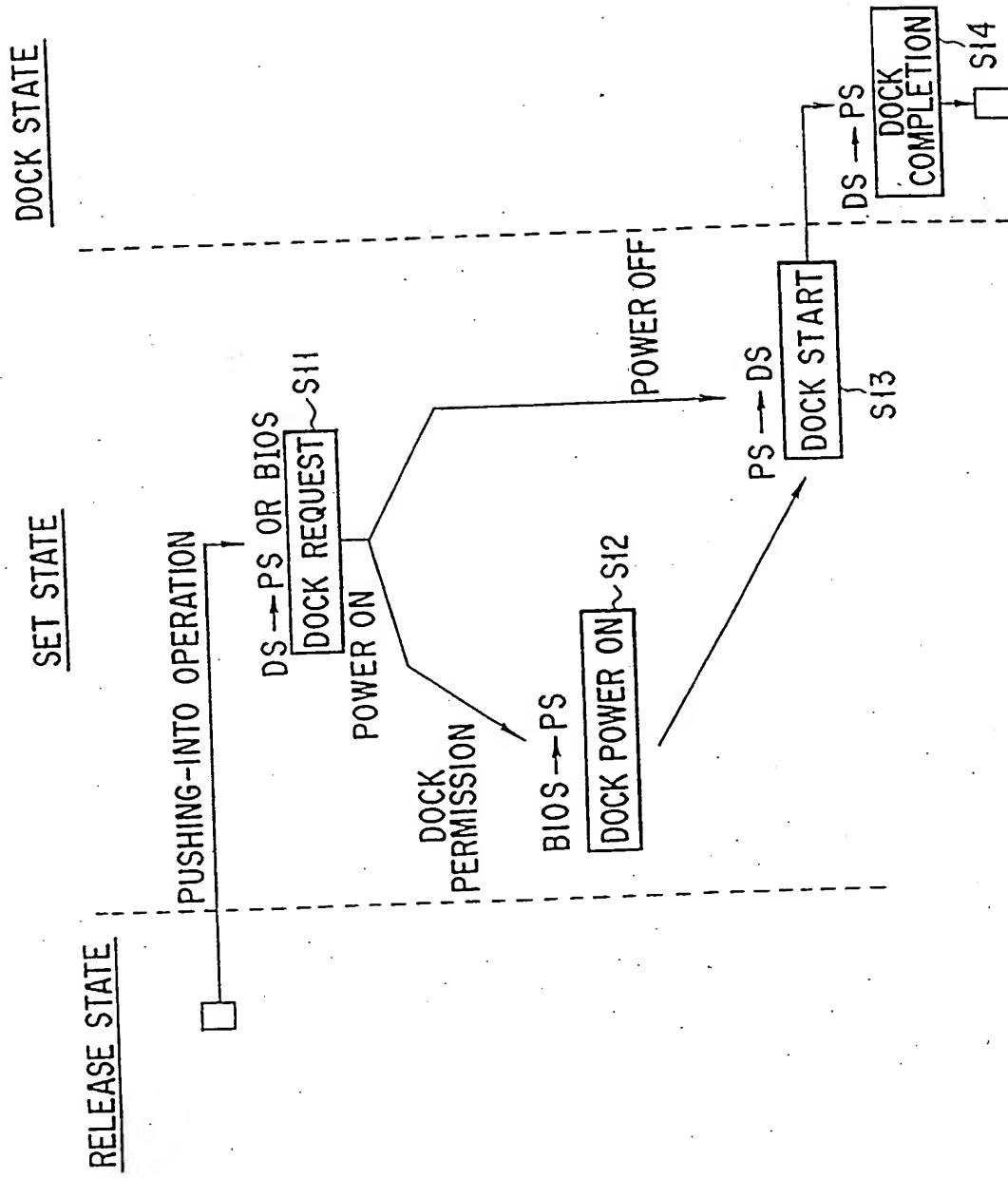


FIG. 6A

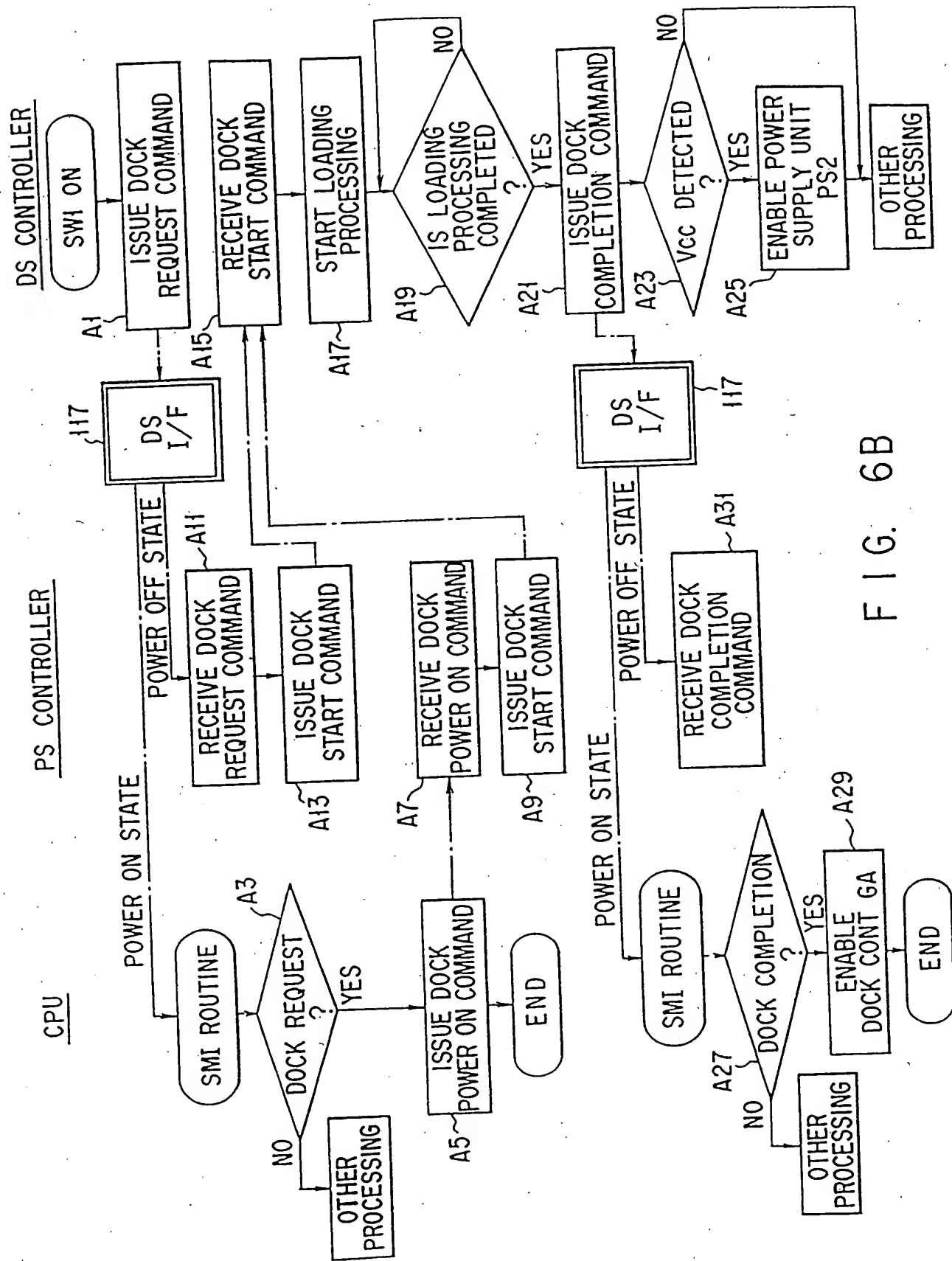
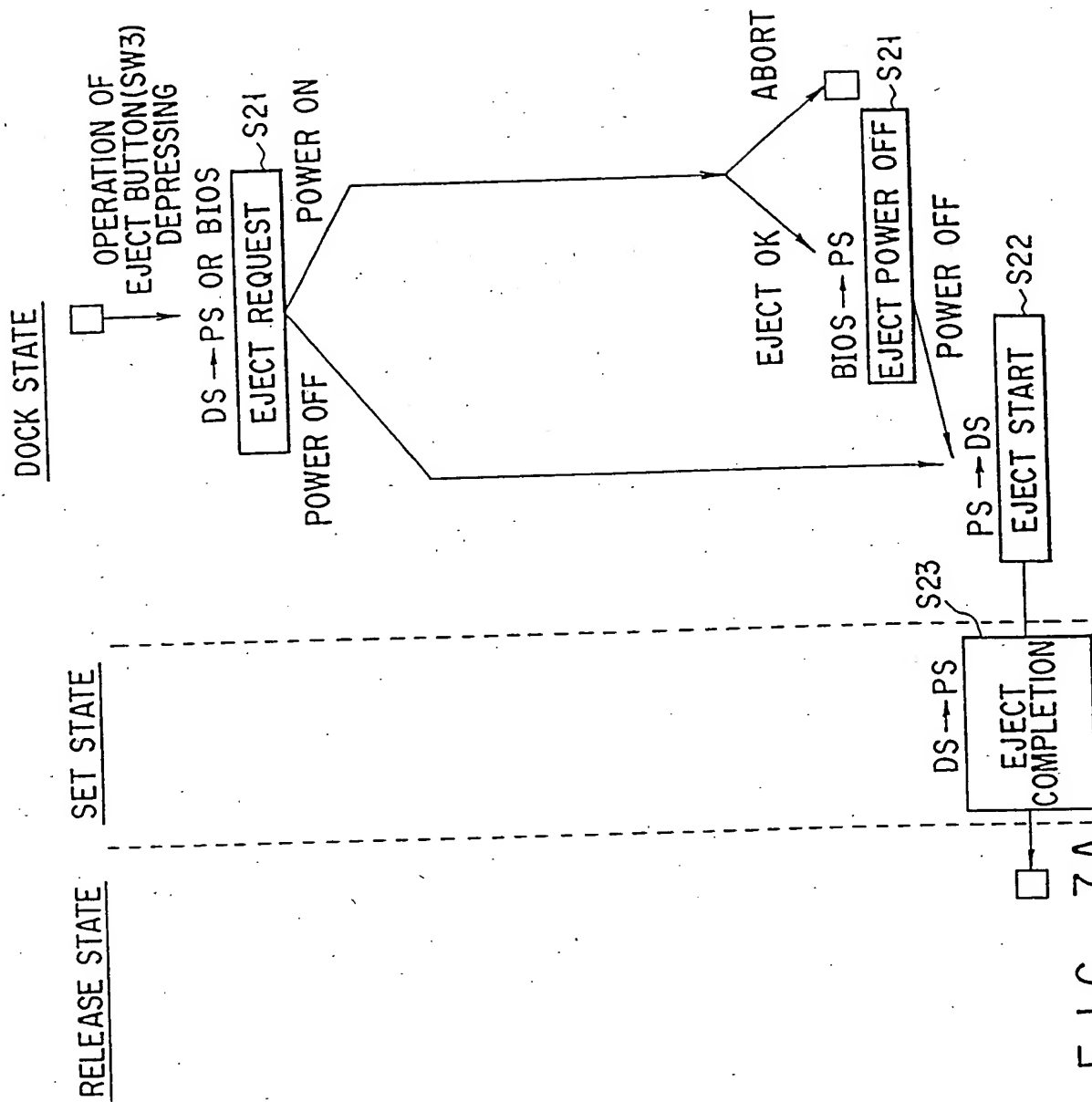


FIG. 6B



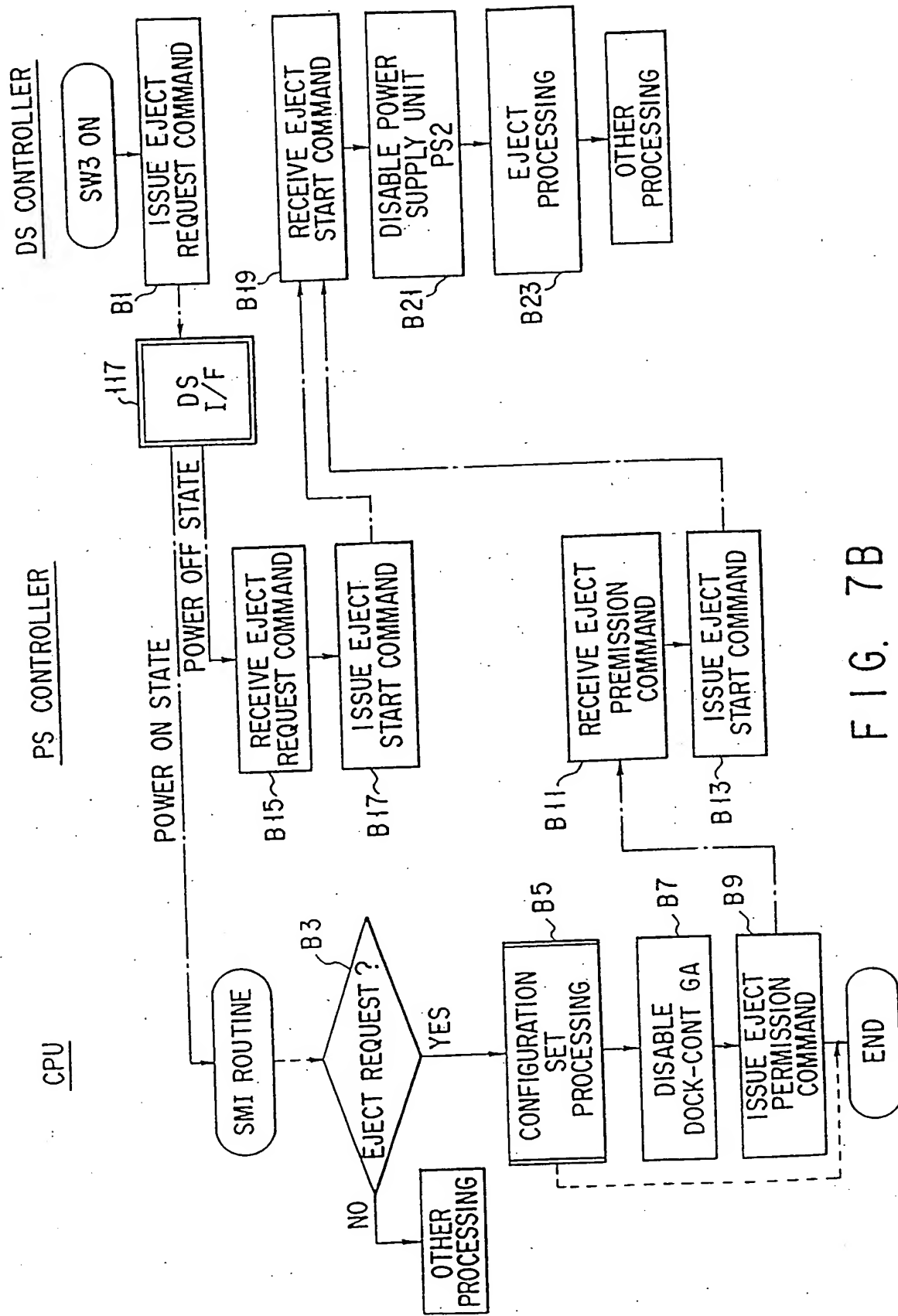


FIG. 7B

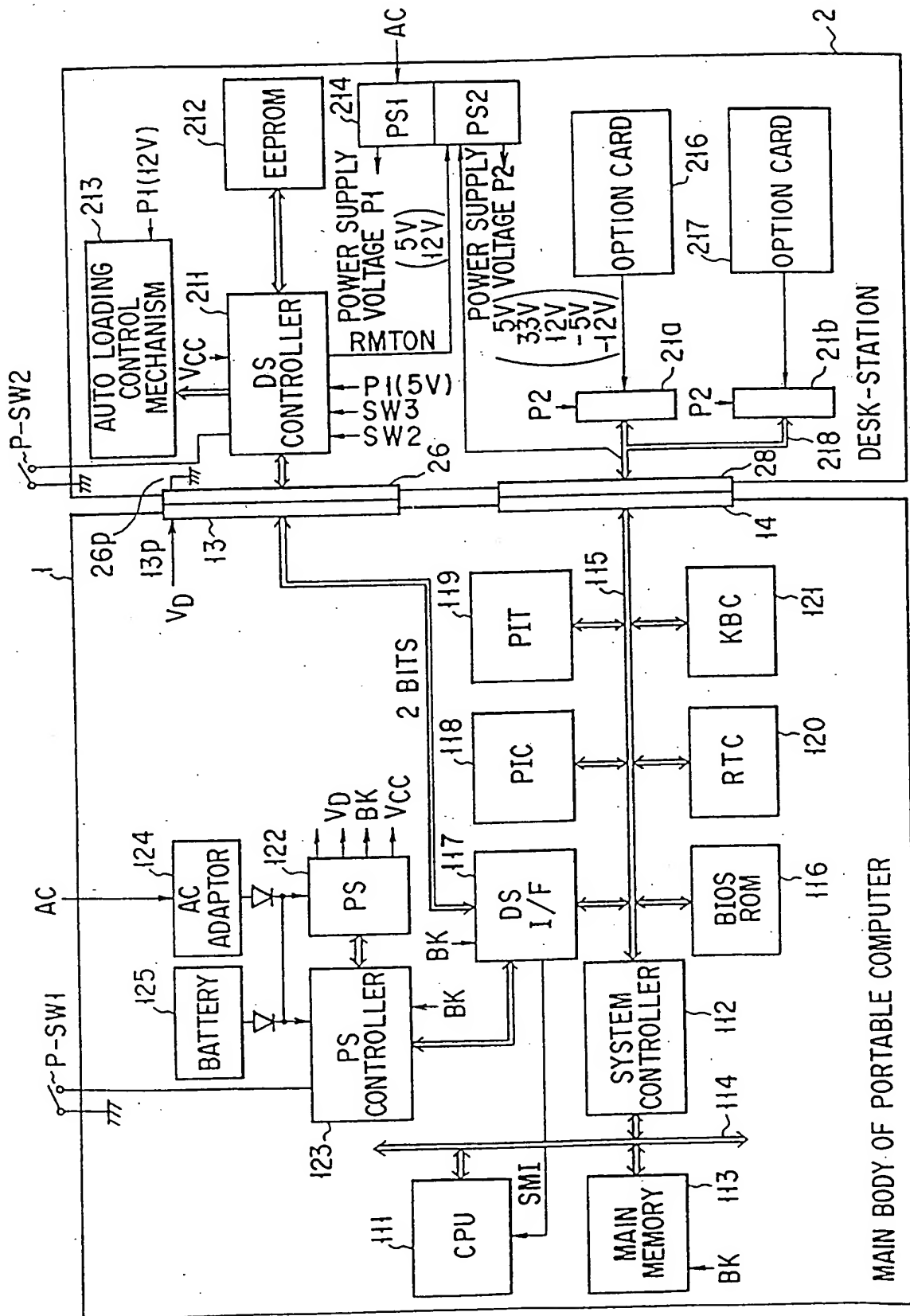


FIG. 8

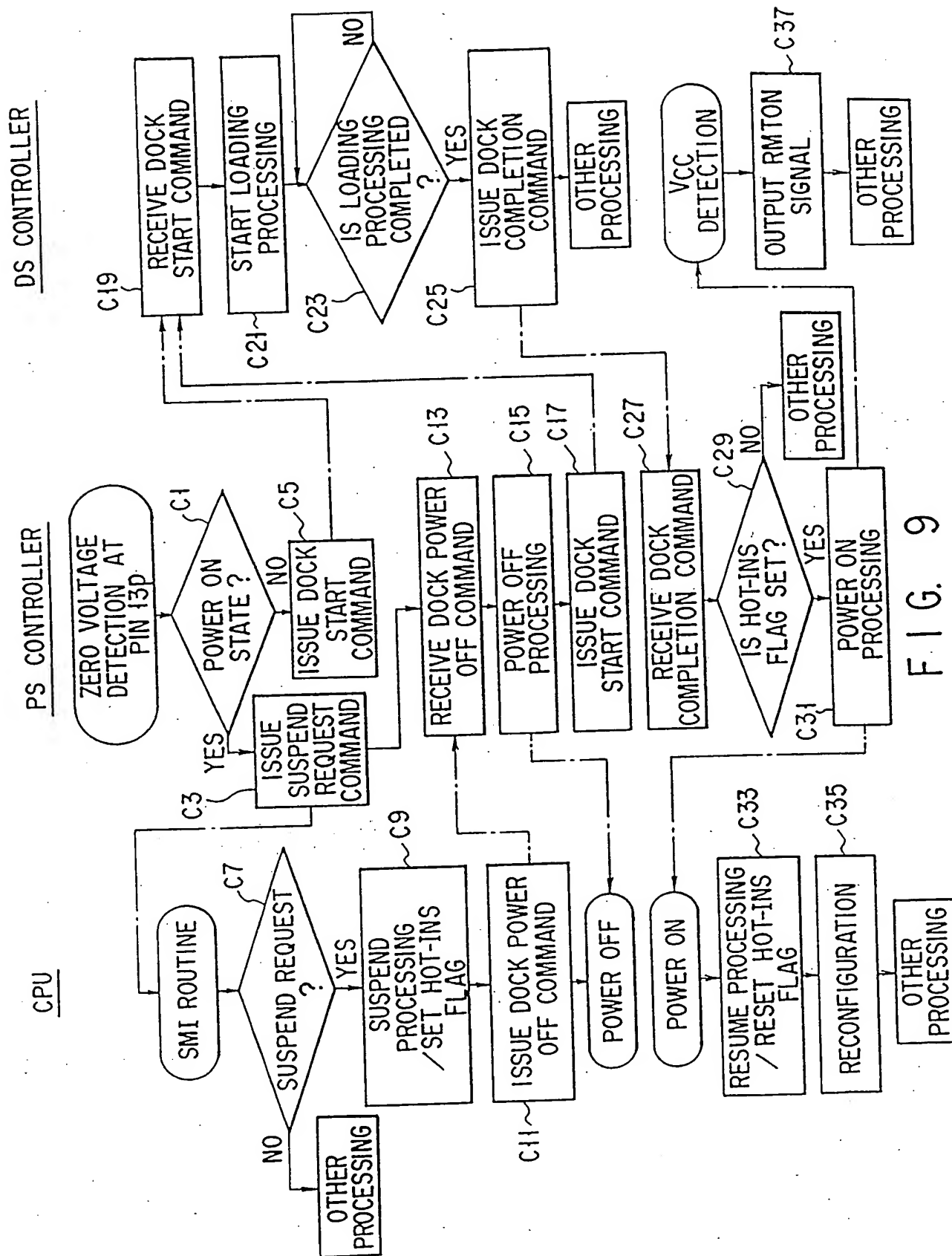


FIG. 9

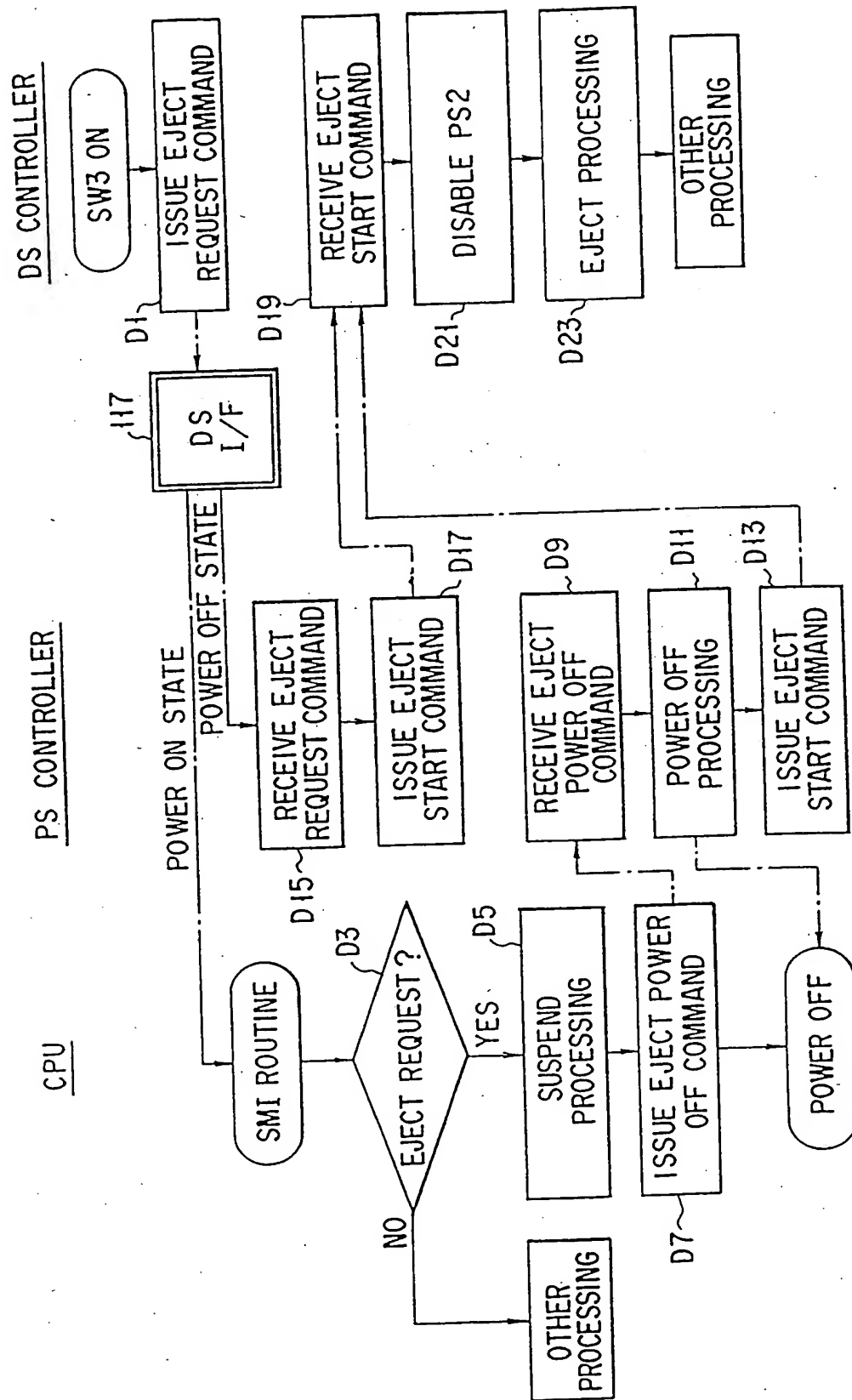


FIG. 10

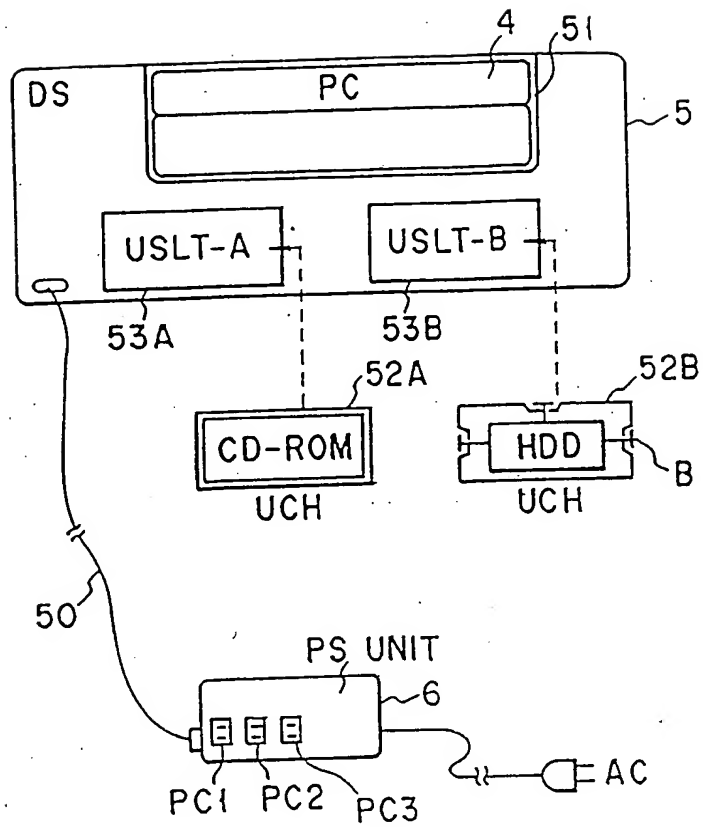


FIG. 11

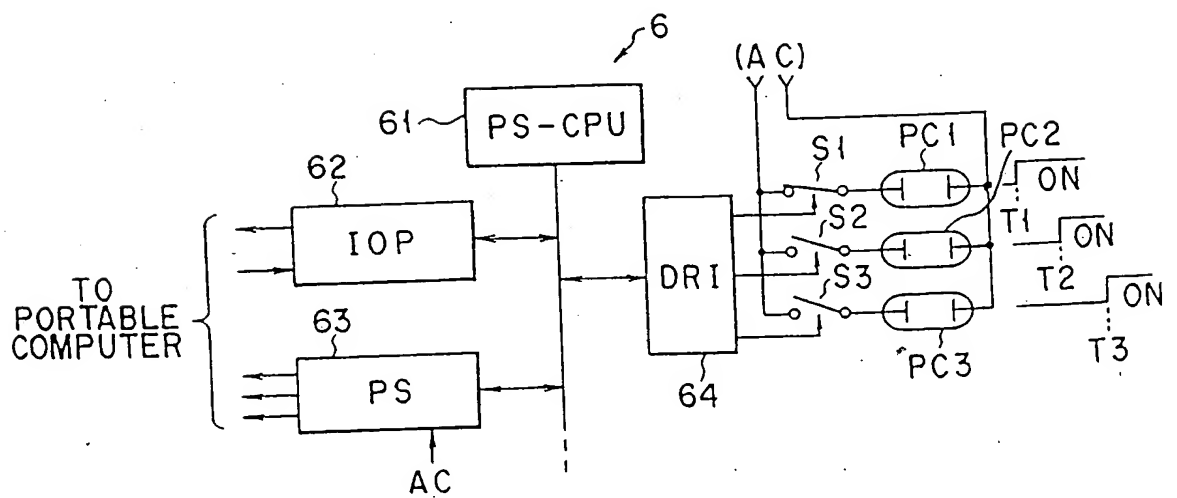


FIG. 12

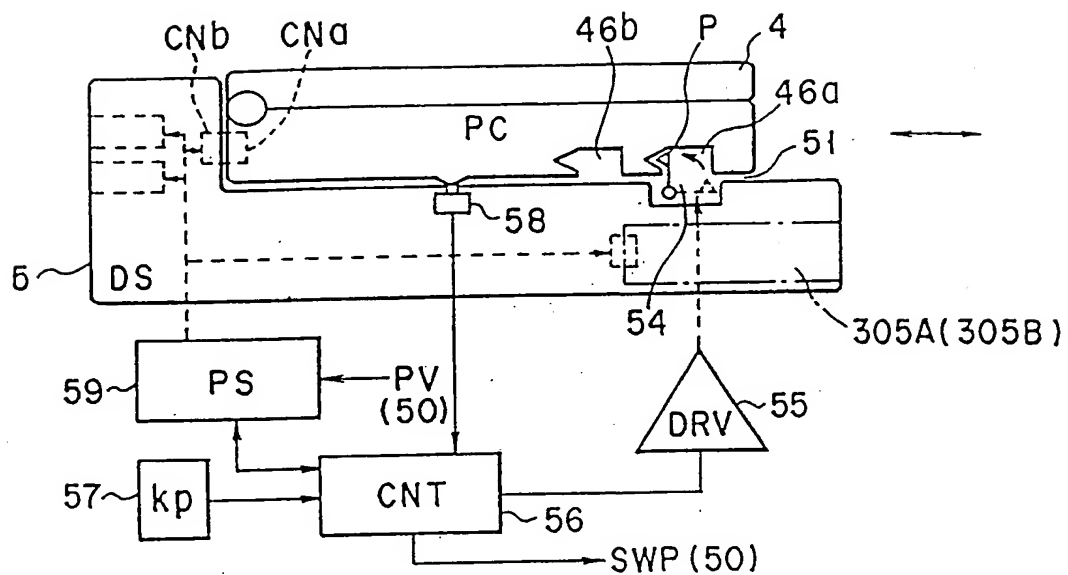


FIG. 13

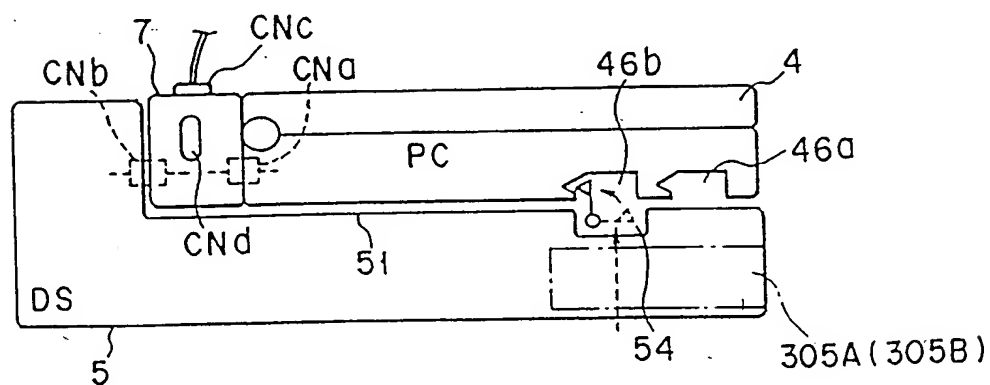


FIG. 14

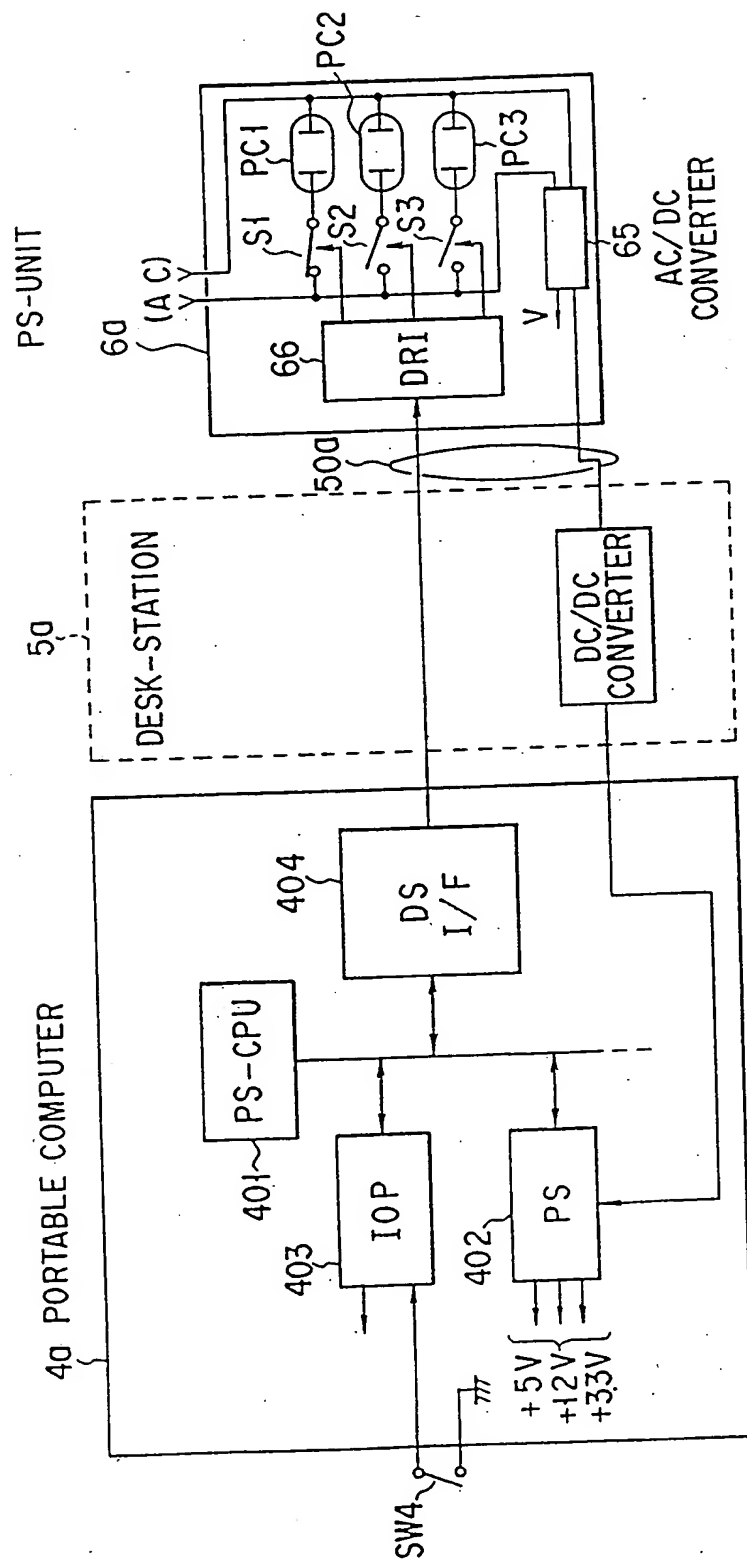


FIG. 15

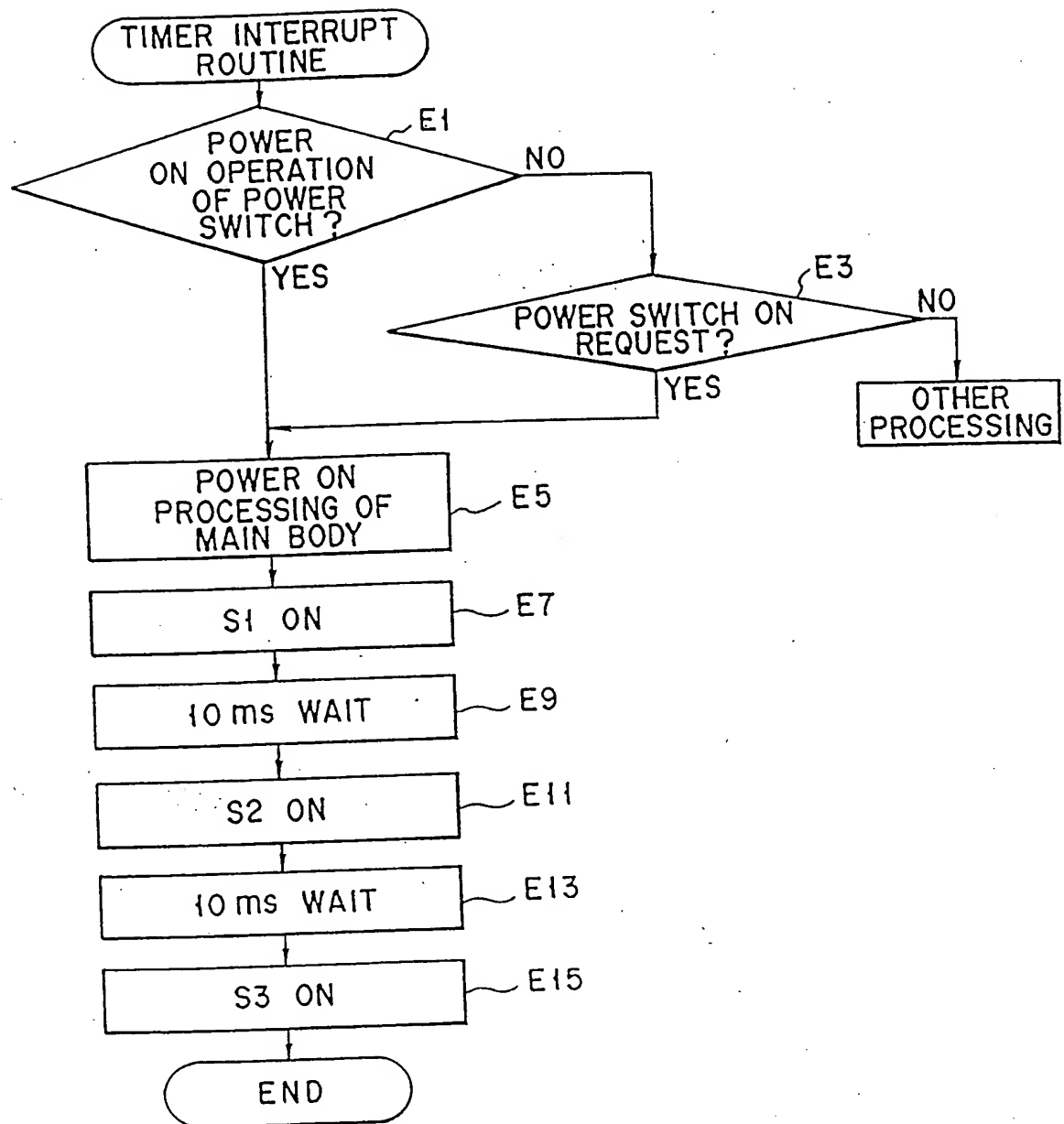
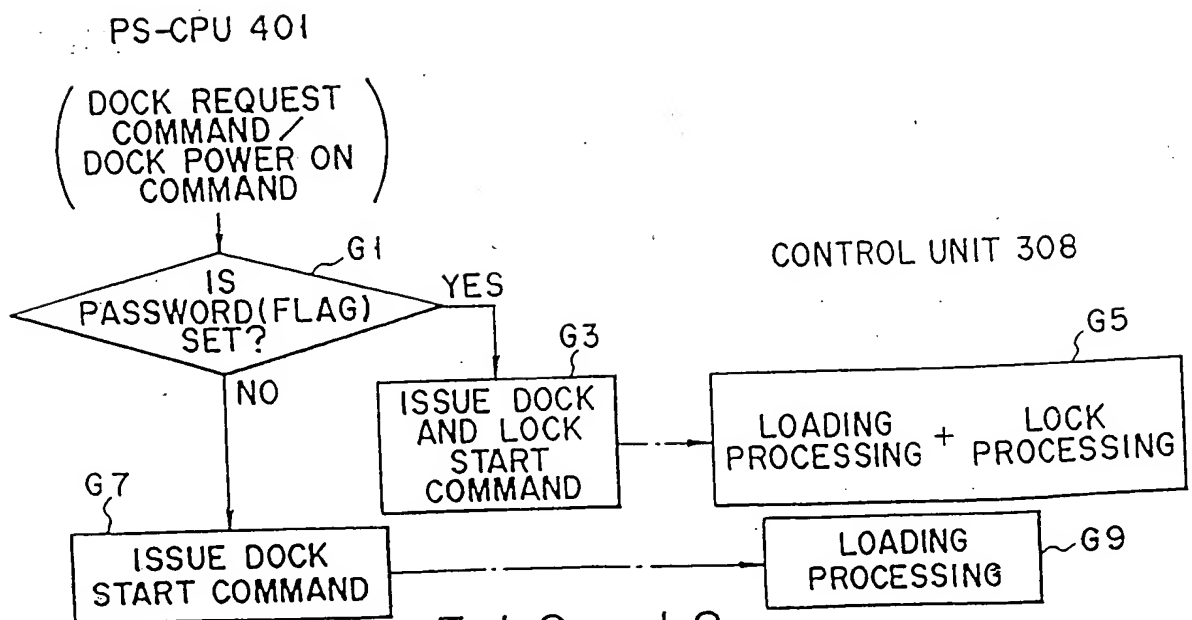
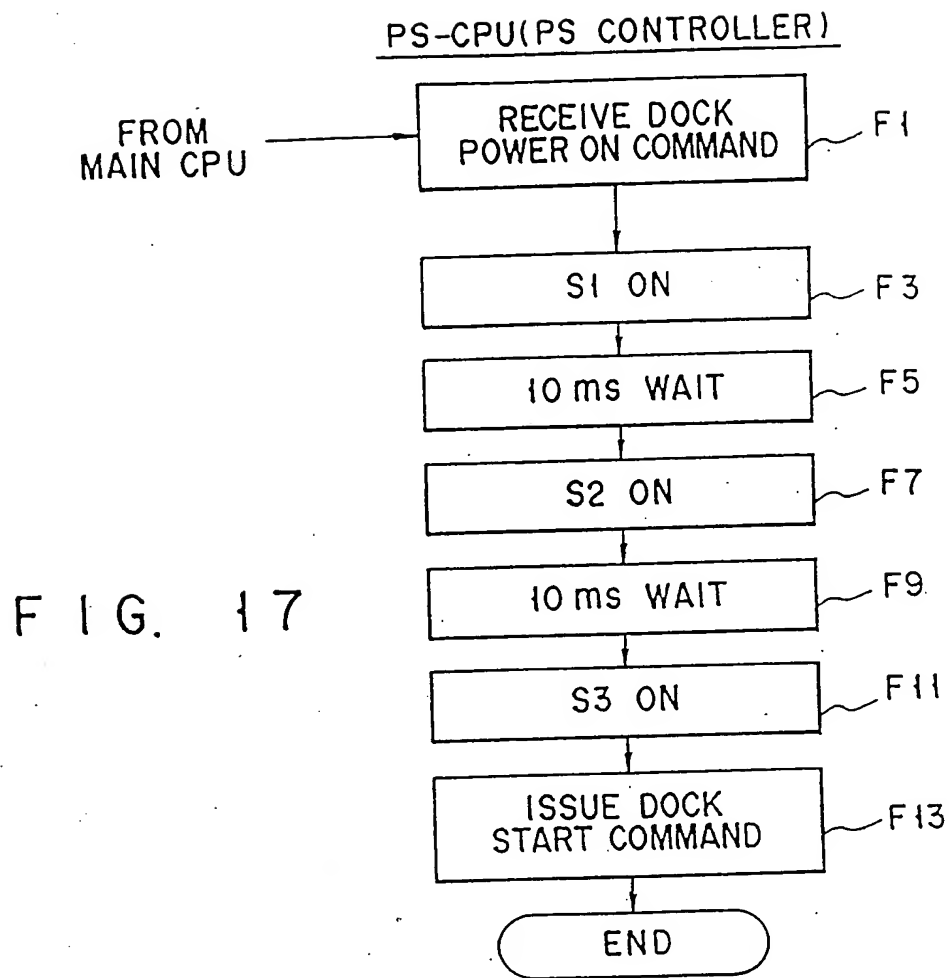


FIG. 16



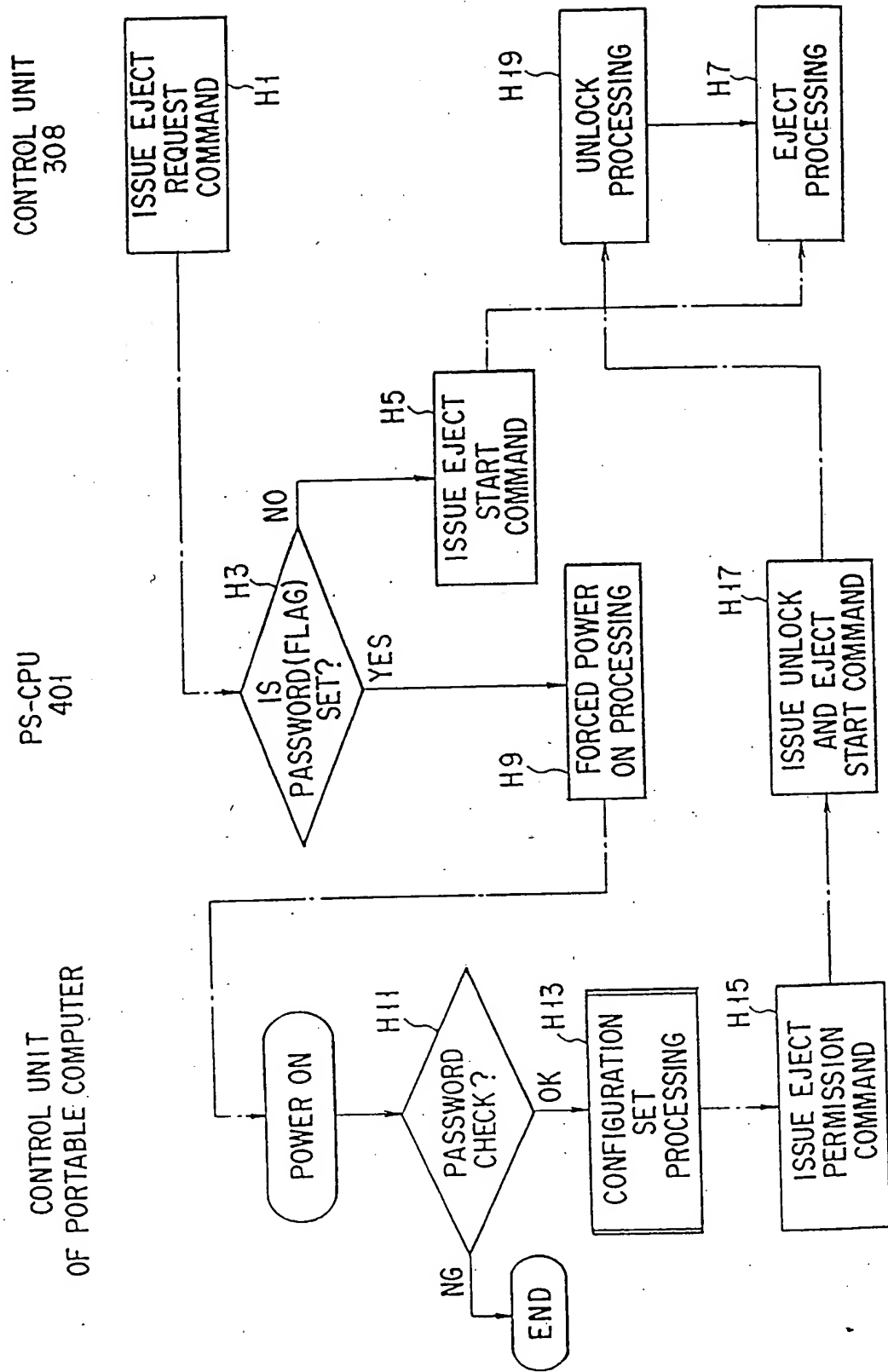


FIG. 19

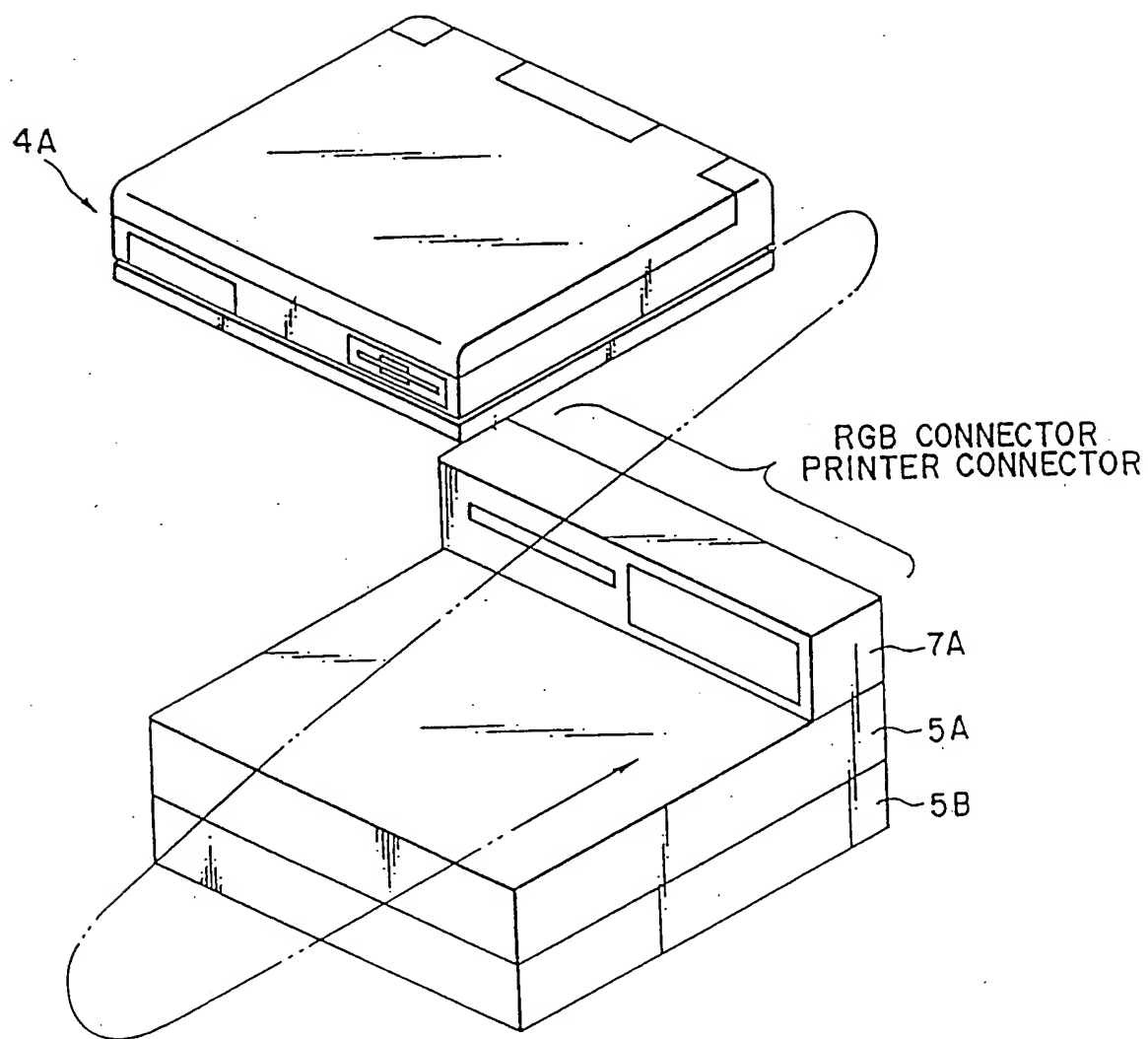


FIG. 21

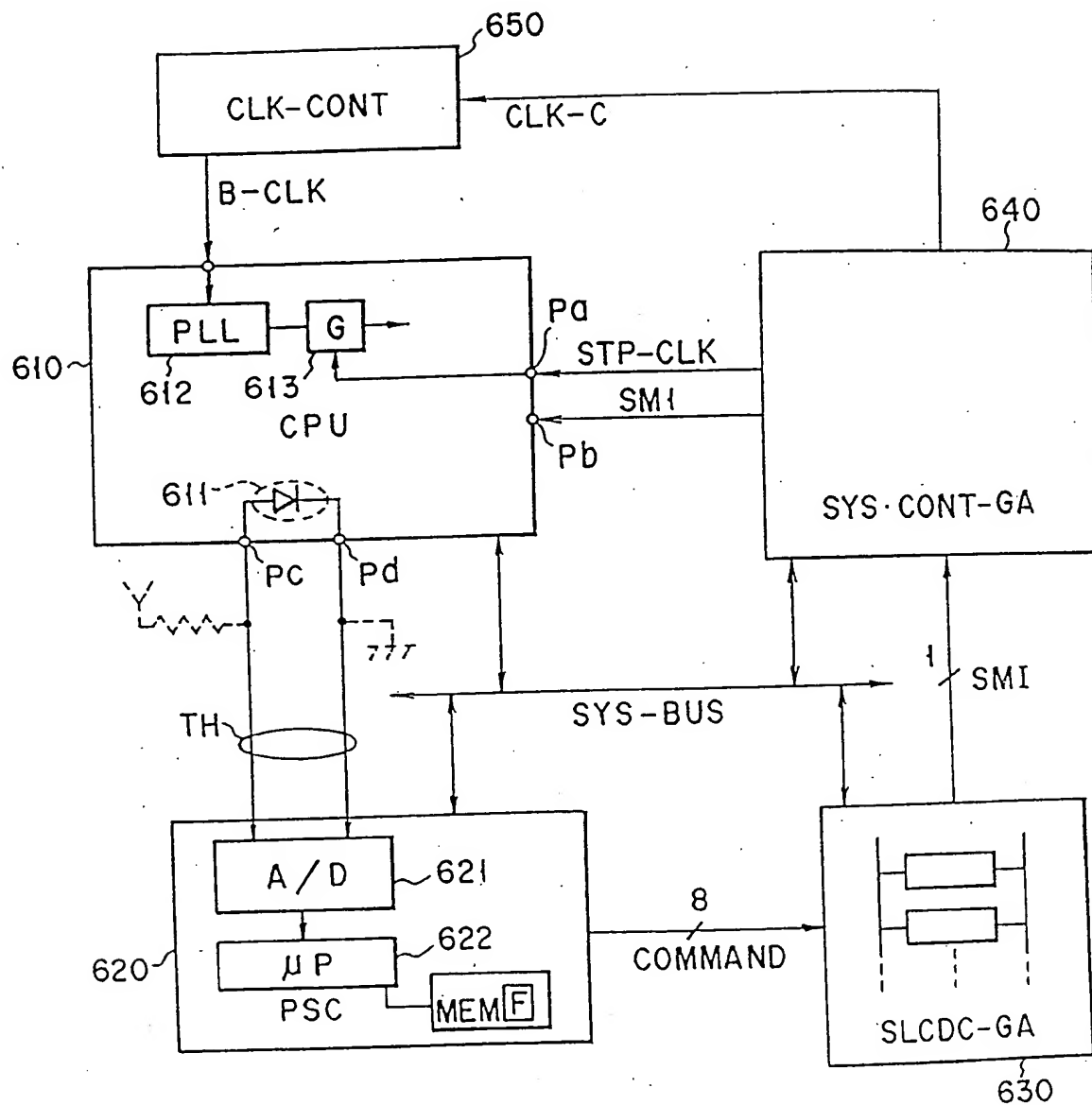


FIG. 22

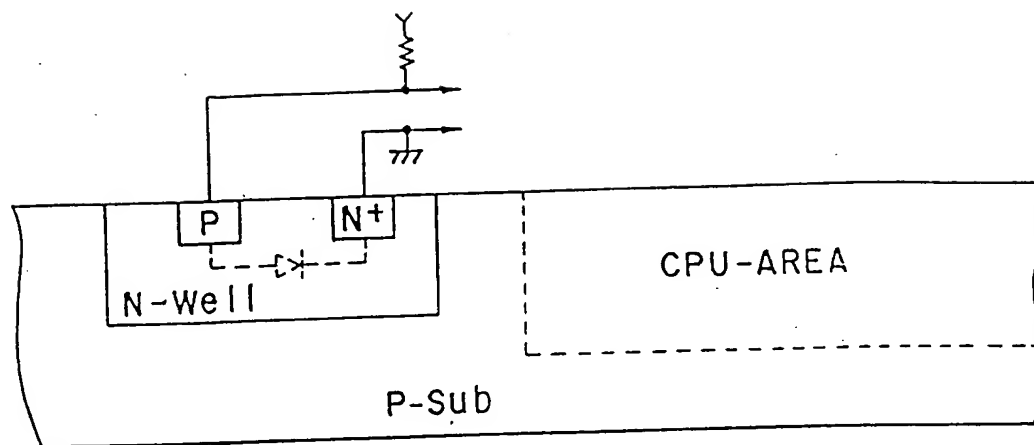


FIG. 23A

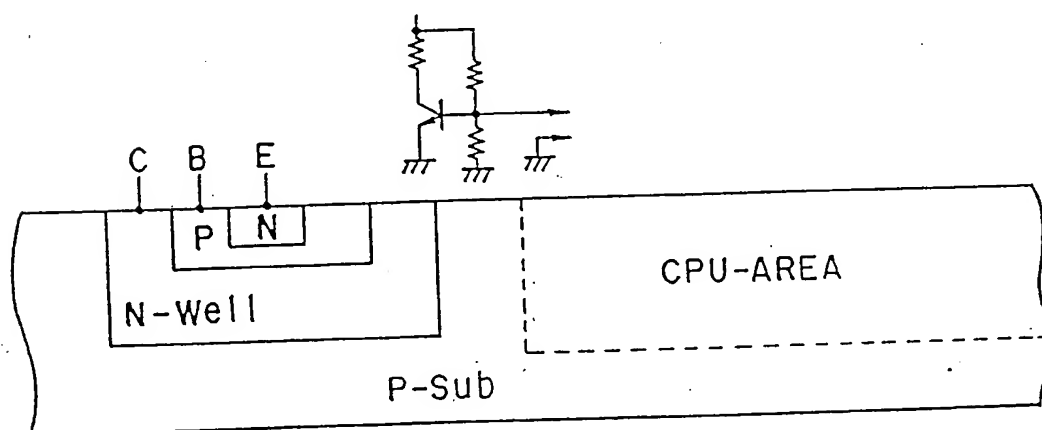


FIG. 23B

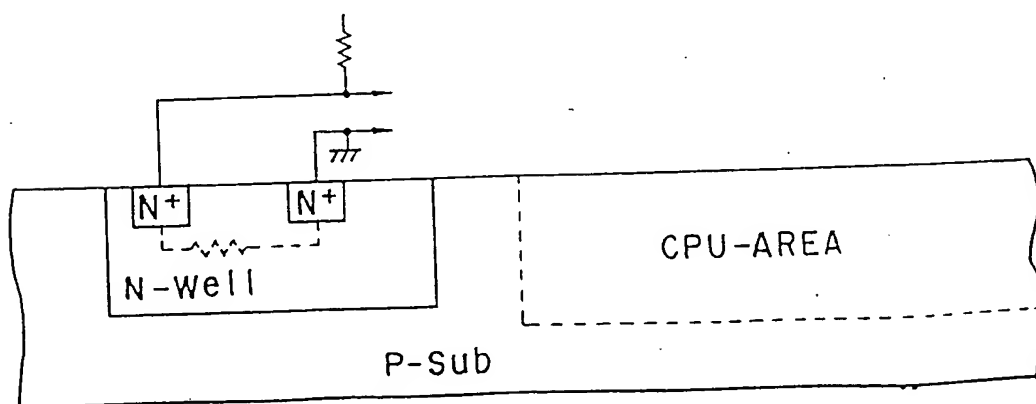


FIG. 23C

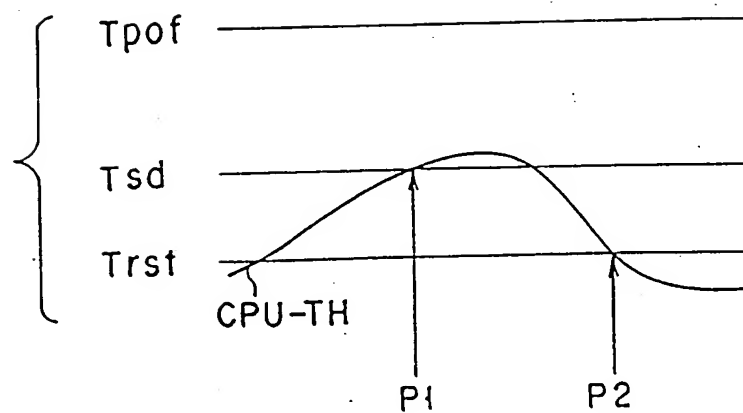


FIG. 24

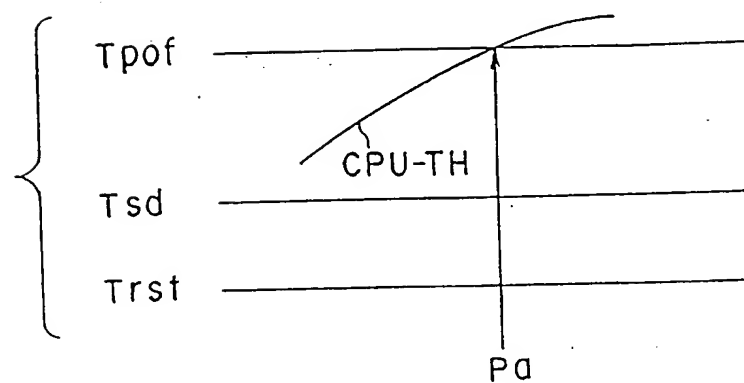


FIG. 25

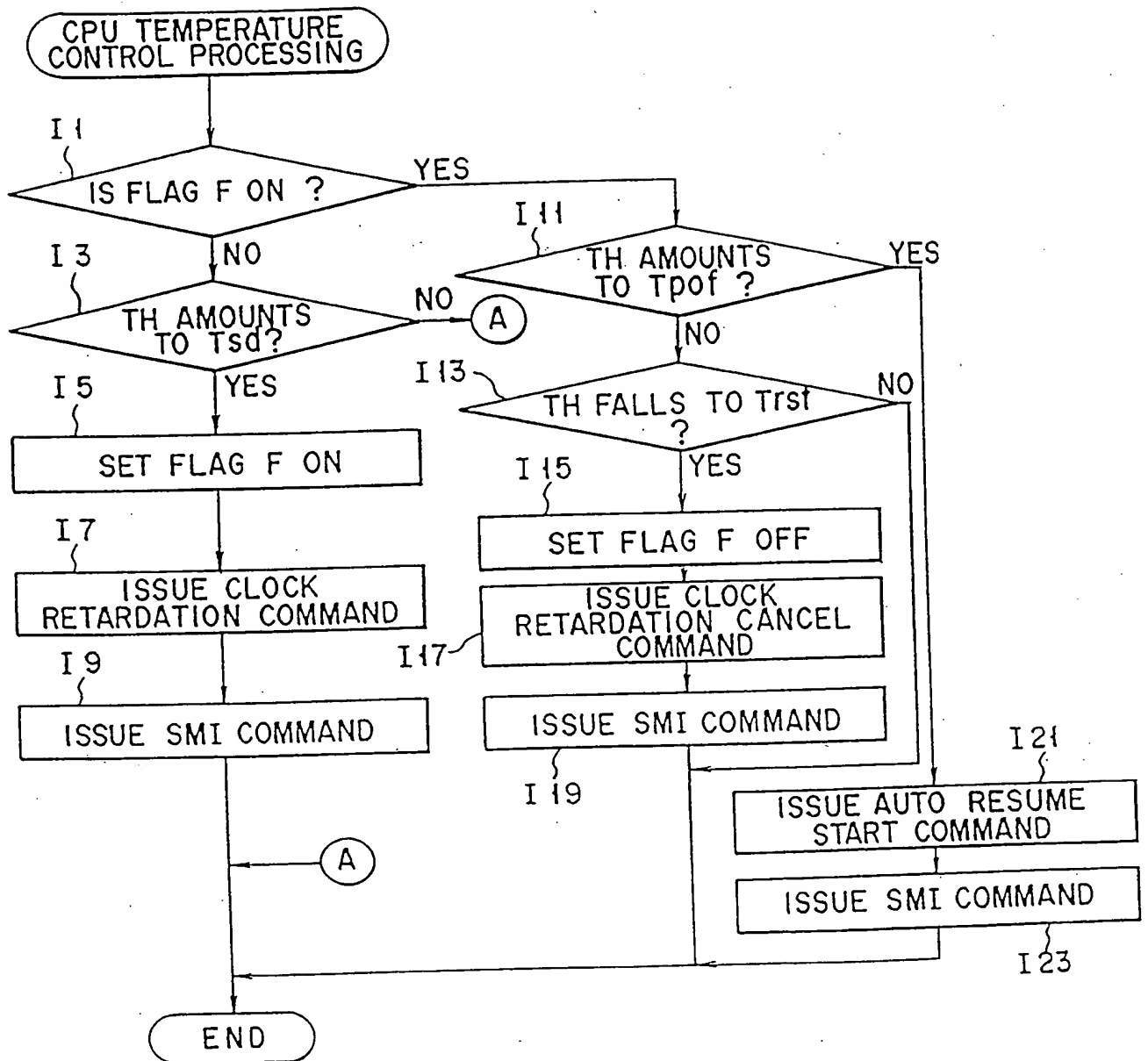


FIG. 26

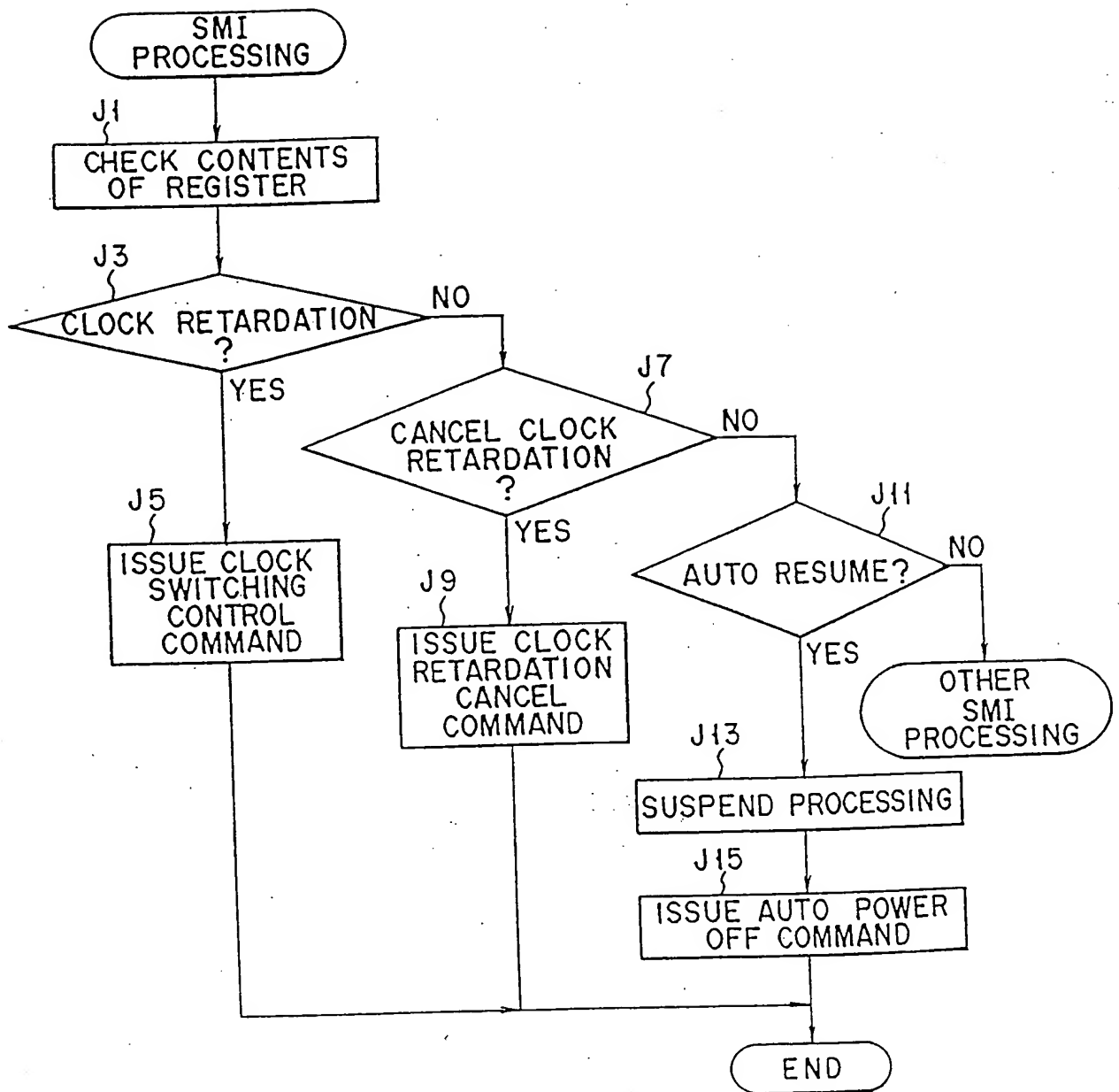


FIG. 27

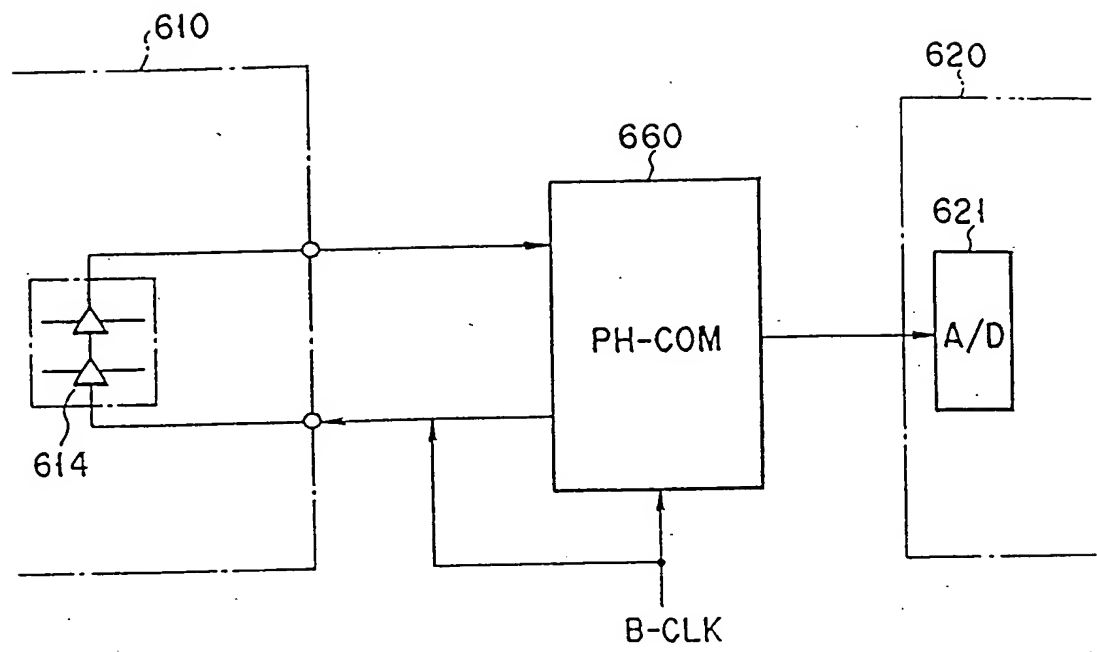


FIG. 28

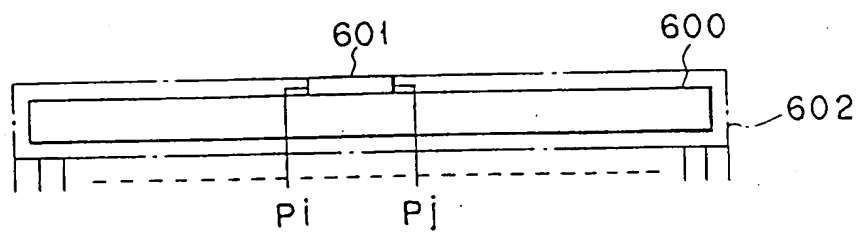
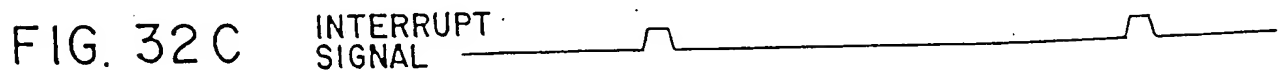
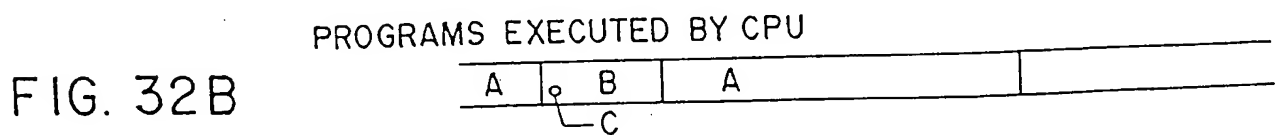
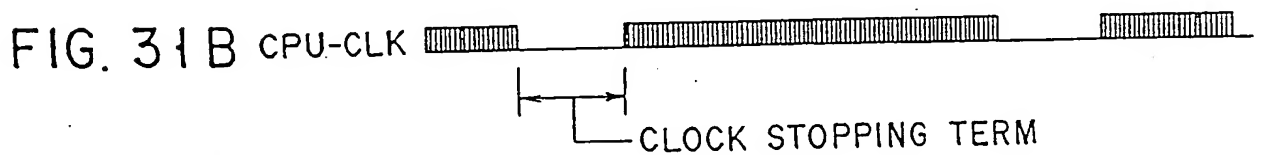
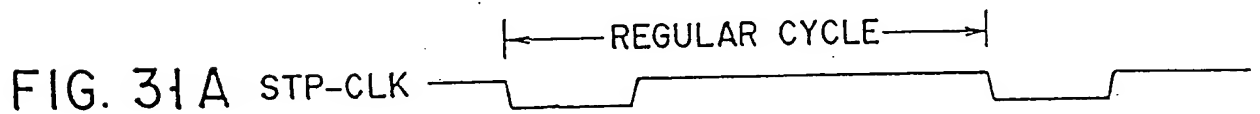
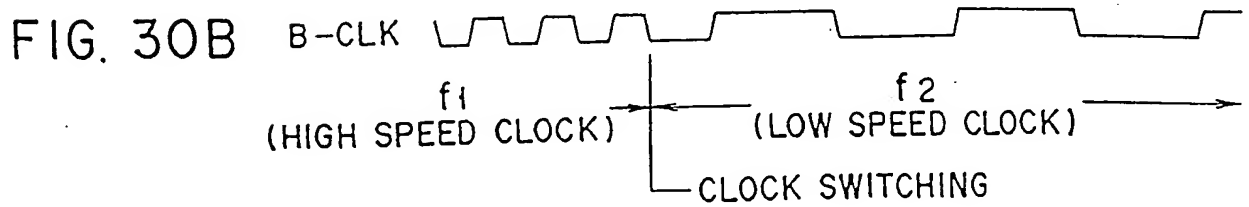
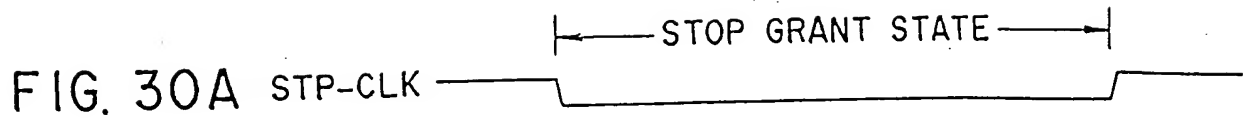


FIG. 29



A : NORMAL PROGRAM PROCESSING
 B : SMI PROCESSING ROUTINE
 C : HALT INSTRUCTION AND HALT-STATE
 IN SMI PROCESSING ROUTINE

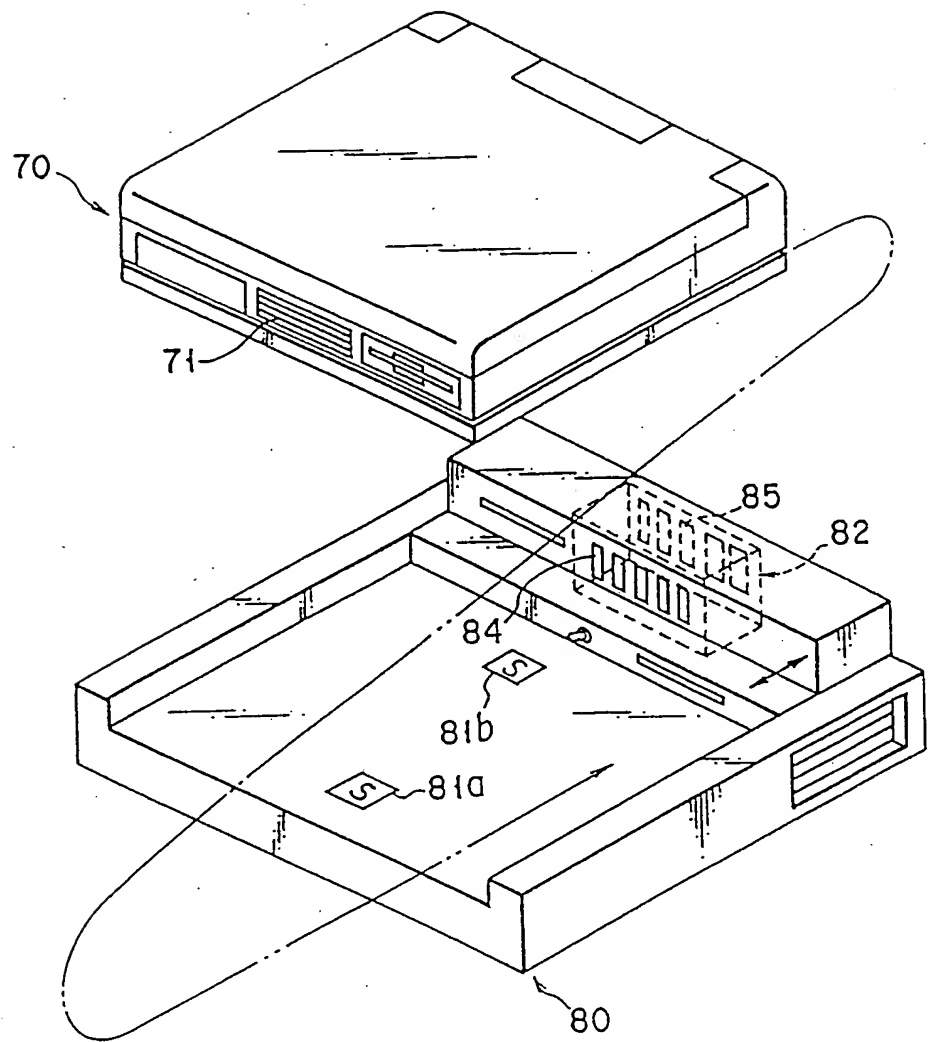


FIG. 33A

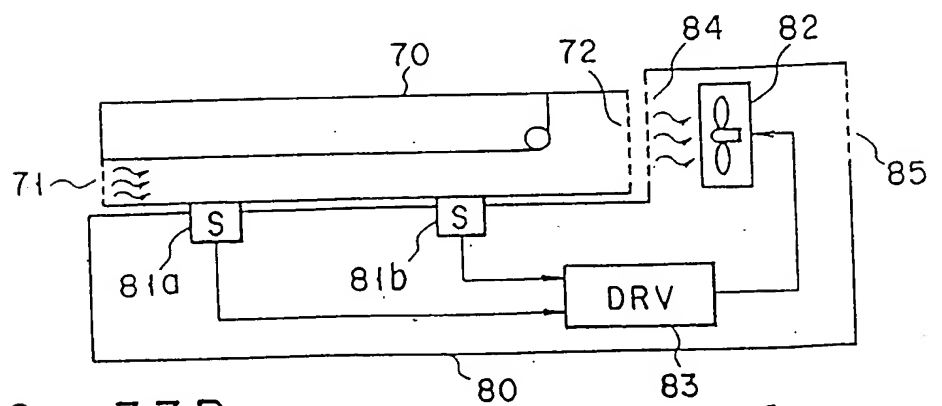
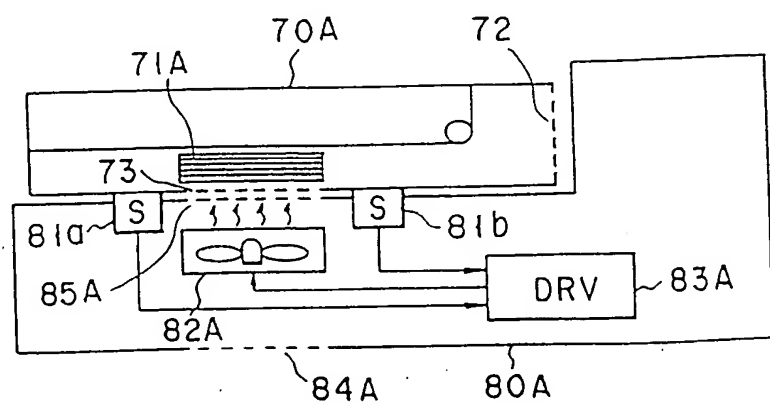
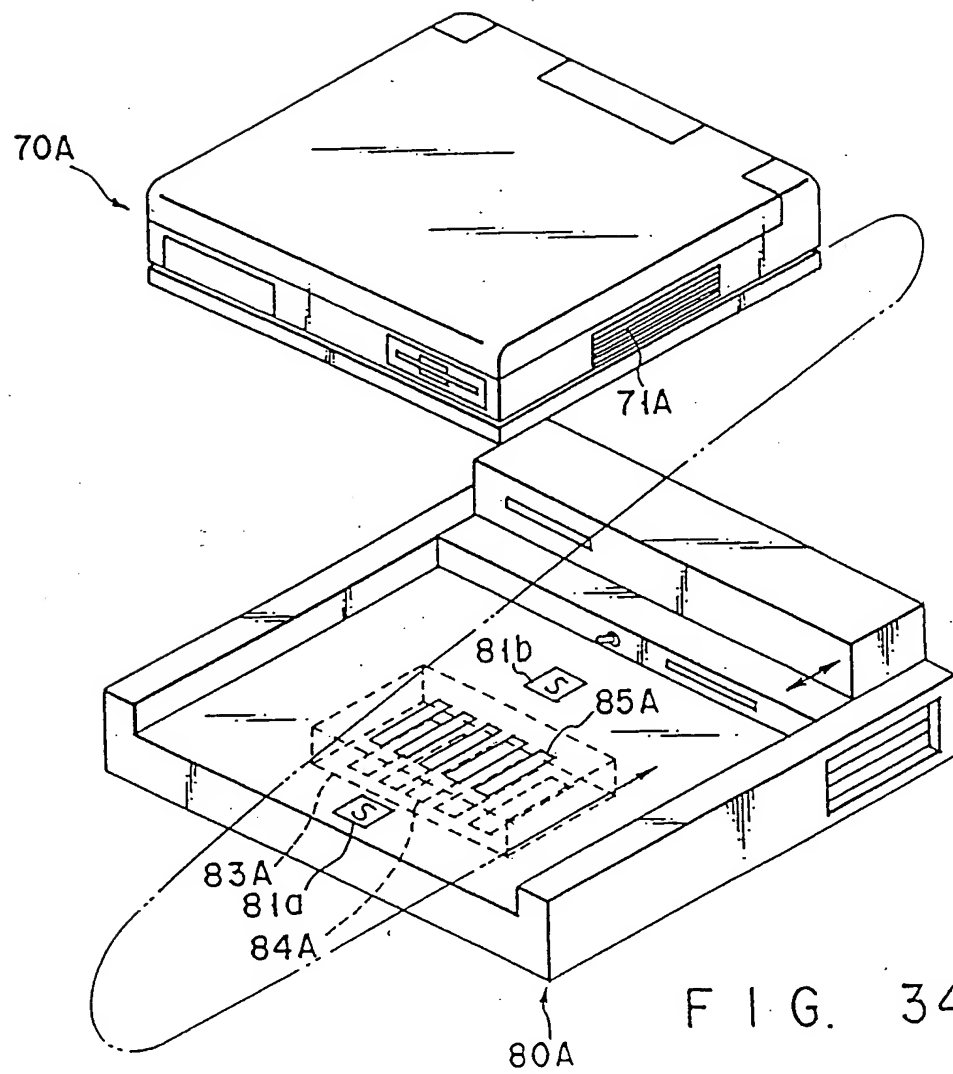


FIG. 33B



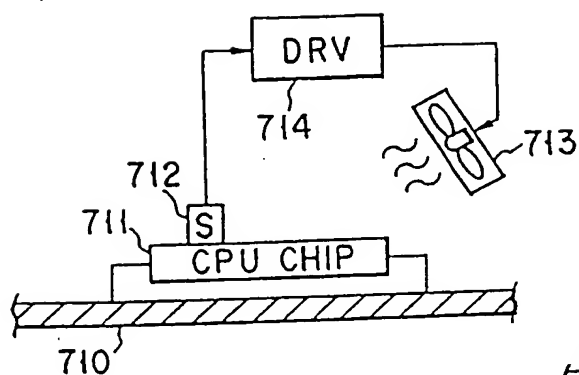


FIG. 35

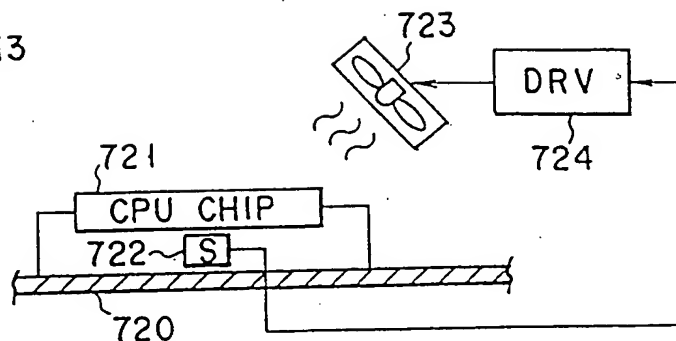


FIG. 36

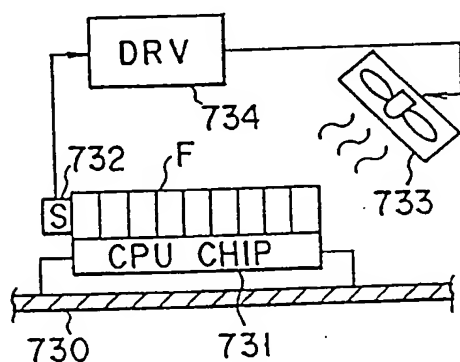


FIG. 37

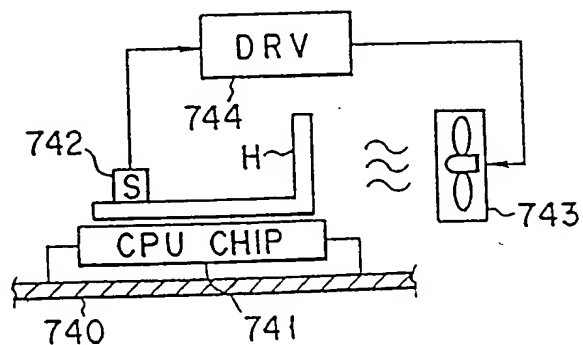


FIG. 38

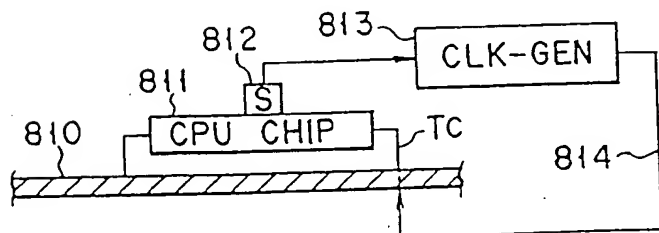


FIG. 39

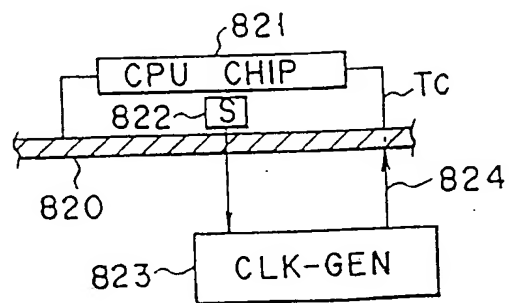


FIG. 40

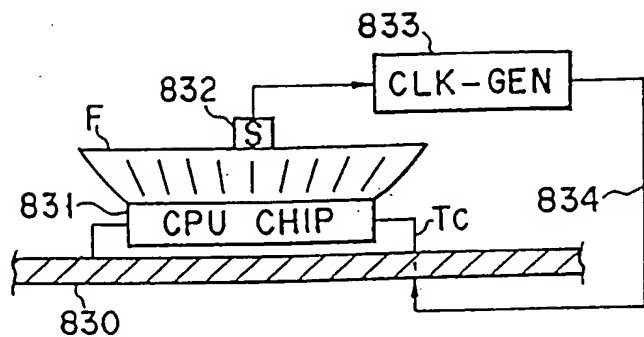


FIG. 41

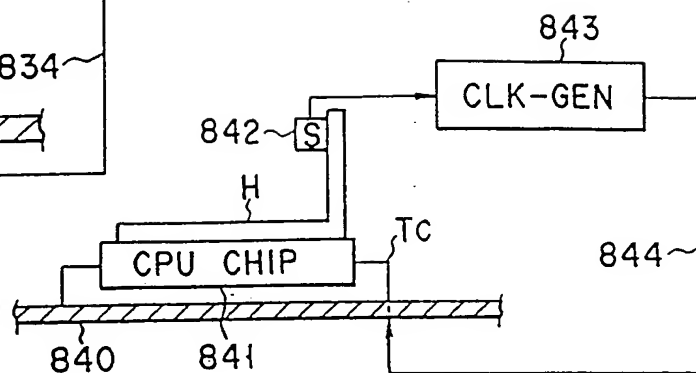


FIG. 42

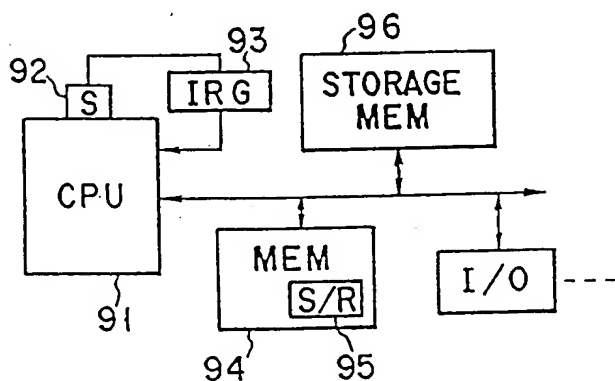


FIG. 43

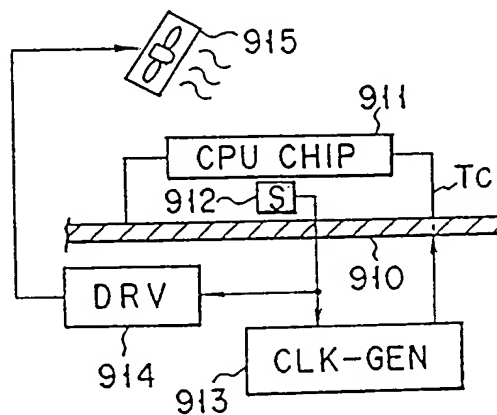


FIG. 44

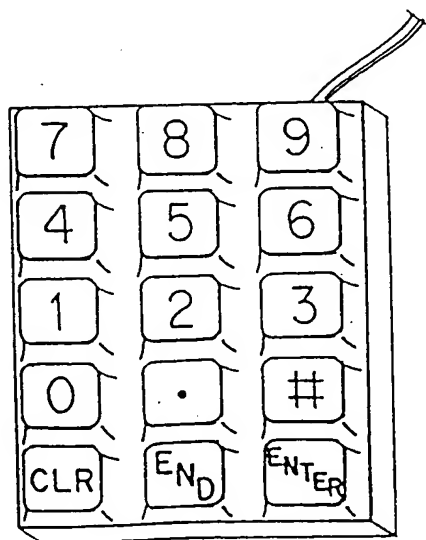


FIG. 45

